OTHER INFORMATION (Including Author, Title, Date, Pertinent Pages, Etc.)

*Examiner Initial	Cite No.	
50	01	Cobb, N., et al., "Experimental Results on Optical Proximity Correction with Variable Threshold Resist Model," Optical Microlithography X, The International Society for Optical Engineering 3051:458-468, March 1997.
50	O2	Cobb, N.B., "Fast Optical and Process Proximity Correction Algorithms for Integrated Circuit Manufacturing," Ph.D. dissertation, University of California at Berkeley, Spring 1988, pp. 64-71.
10	О3	Cobb, N., et al., "Mathematical and CAD Framework for Proximity Correction," Optical Microlithography IX, The International Society for Optical Engineering 2726: 208-222, March 1996.
210	04	Benchmark Technologies, OPC Reference Standard (J111A) Reticle, October 12, 1999.
10	O5	"Resolution Enhancement Technologies (OPC/PSM)," July 16, 2002, Mentor Graphics Internet Site, Technical Papers.
<u>To</u>	O6	Randall, J., et al., "Lithography Simulation With Aerial Image - Variable Threshold Resist Model," <i>International Conference on Micro and Nano Engineering 46</i> :59-60, September 1998.
<u>J0</u>	O7	Rieger, M.L. and J.P. Stirniman, "Using Behavior Modeling for Proximity Correction," SPIE Proceedings, The International Society for Optical Engineering, Optical/Laser Microlithography VII 2197:371-376, May 1994.
JO	O8	Schellenberg, F.M., et al., "SEMATECH J111 Project: OPC Validation," SPIE Proceedings, Optical Microlithography XI 3334:892-911, 1998.
<u>Jo</u>	09	Schellenberg, F.M., "Design for Manufacturing in the Semiconductor Industry: The Litho/Design Workshops," <i>Proceedings of the 12th Int'l. Conference on VLSI Design</i> , Jan. 10-13, 1999, pp. 111-119.
JO	O10	Schellenberg, F.M., "Sub-Wavelength Lithography Using OPC," Semiconductor Fabtech, 9th ed., March 1999.
10	O11	Spence, C., et al., "Integration of Optical Proximity Correction Strategies in Strong Phase Shifters Design for Poly-Gate Layers," SPIE Proceedings, 19th Annual Symposium of Photomask Technology 3873:277-287, 1999.

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O12 SPIE Proceedings, 19th Annual Symposium on Photomask Technology 3873:21, Editors: Abboud, F. et al., 1999.

O13 SPIE Proceedings, Optical Microlithography DX 2726:15, Editors. Fuller, G.E. et al., 1996.

O14 Stirniman, J.P. and M.L. Rieger, "Optimizing Proximity Correction for Wafer Fabrication Processes," *Proc. SPIE, 14th Annual BACUS Sympoosium on Photomask Technology and Management 2322*:239-246, December 1994.

O15 Washington, A., *Basic Technical Mathematics with Calculus*, 2d ed., 1970, pp. 245-247, 260-262, 505-525.

Examiner

Date Considered

11/06/06

*Examiner: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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